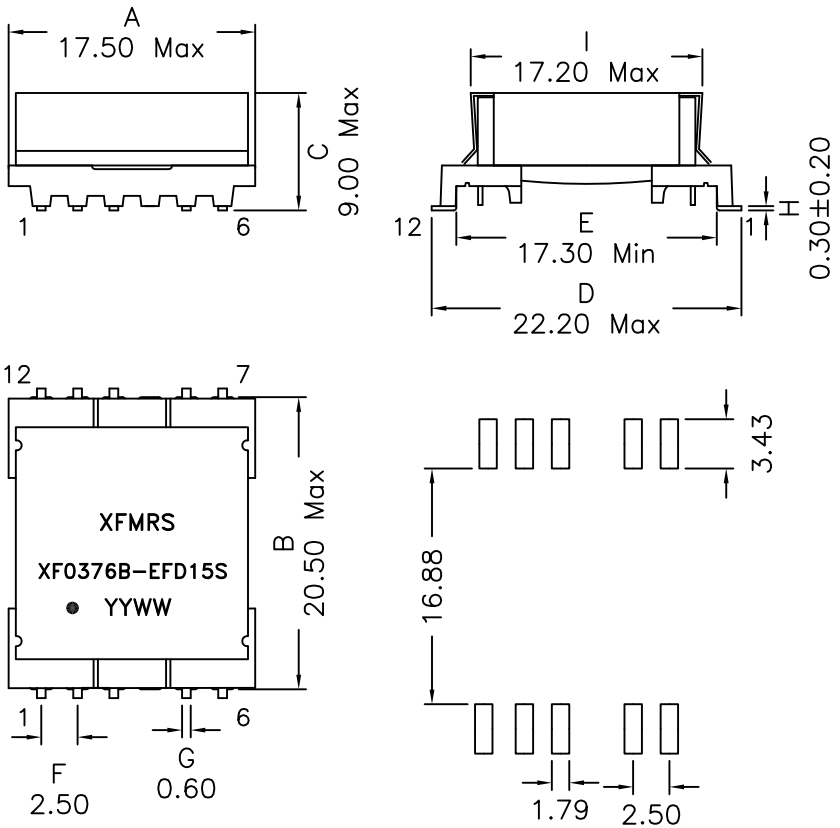
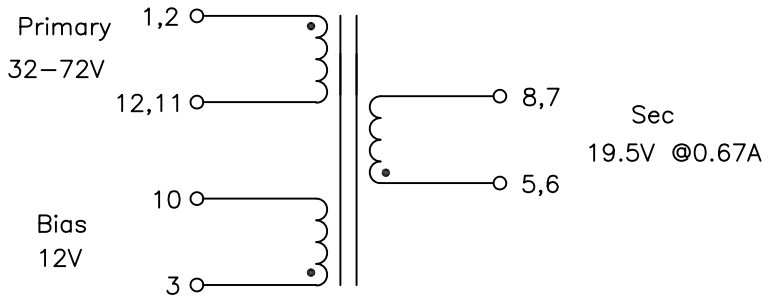
1. Mechanical Dimensions:



Suggested PCB Layout

2. Schematic:



3. Electrical Specifications: @25°C

OCL: Pins 1,2–11,12 37uH±10% @250KHz, 0.3V
Pins 1,2–11,12 31.5uH Min @250KHz, 0.3V, 2.0Adc
Q: Pins 1,2–11,12 15 Min @250KHz,0.3V
LL: Pins 1,2–11,12 0.50uH Max @300KHz, 1.0V Short 5+6+7+8
CW/w: Pins 1,2–5,6 160pF Max @300KHz, 1.0V Short Pri & Bias; Sec
DCR: Pins 1–12 0.137 Ohms Max
Pins 2–11 0.170 Ohms Max
Pins 5–8 0.078 Ohms Max
Pins 6–7 0.078 Ohms Max
Pins 3–10 0.395 Ohms Max
Turns Ratio: (1,2–12,11):(5,6–8,7):(3–10) = 1.000:0.565:0.348±2%
Hipot: 1500Vrms, Pri & Bias to Sec
500Vrms, Pri to Bias
SRF: Pins 1,2–11,12 2.0MHz Min

Notes:

1. Solderability: Leads shall meet MIL–STD–202G, Method 208H for solderability.
2. Flammability: UL94V–0
3. ASTM oxygen index: > 28%
4. Insulation System: Class F 155°C. UL file E151556
5. Operating Temperature: –40°C to +85°C
6. Storage Temperature Range: –55°C to +125°C
7. Aqueous wash compatible
8. SMD Lead Coplanarity: 0.004" (0.102mm) Max
9. Electrical and mechanical specifications 100% tested
10. RoHS Compliant Component
11. Removed Pins 4 & 9
12. Recommended IR Reflow peak temp of 250°C Max.

DOC REV: B/1

XFMRS, Inc www.XFMRS.com	Title: 13W PoE Xfmr		
UNLESS OTHERWISE SPECIFIED TOLERANCES: .xx ±0.25 Dimensions in MM	P/N: XF0376B-EFD15S		REV. B
	DWN.	Huo	Jul–07–17
	CHK.	YK Liao	Jul–07–17
SHEET 1 OF 1	APP.	BW	Jul–07–17

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